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Applicant(s): Belyansky, et al.

Docket No.

FIS920040047US1

Application No.
10/709,652Filing Date
May 20, 2004Examiner
Walter Lee Lindsay Jr.Group Art Unit
2812

Invention: REDUCED DIELECTRIC CONSTANT SPACER MATERIALS INTERGRATION FOR HIGH SPEED LOGIC GATES

I hereby certify that this Amendment After Final
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Appl. No. 10/709,652
Arndt. Dated 01/04/2006
Reply to Office action of November 29, 2005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Confirmation No. 3651

In re : Application of Belyansky, et al.
For : REDUCED DIELECTRIC CONSTANT SPACER MATERIALS
INTRODUCTION FOR HIGH SPEED LOGIC GATES
Serial No. : 10/709,652
Filed : May 20, 2004
Group Art Unit : 2812
Examiner : Walter Lee Lindsay, Jr.
Our Docket No. : FIS920040047US1
Customer No. : 24036

January 4, 2006

By Fax 571-273-8300

COMMISSIONER FOR PATENTS
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Alexandria, VA 22313-1450

AMENDMENT

Sir:

This is in response to the Final Office Action dated November 29, 2005 having a shortened statutory period of response set to expire on February 28, 2006.

Please amend the referenced application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.